

## **REMARKS**

Applicant thanks Examiner for the detailed review of the application and the suggestion for a direction of potential allowable subject matter.

### **Claim Objections**

The Office Action states:

3. In Claim 55, Line 7, "of" should be inserted after "plurality".

Applicant has inserted a 'y' as suggested.

### **Claim Rejections -35 USC § 102(e)**

5. Claims 1-2, 4-6, 10-12, 15-17, 20, 22-25, 27-28, 32, 35-43, 47, 50-51, 55-58, 62-65, 67-68, and 70-71 are rejected under 35 U.S.C. 102(e) as being anticipated by Kissell (USPN 7,376,954).

The Office Action cites Kissell's ThreadConfig register that is copied from a current thread into another register of another thread. And an inter-thread communication register that allows one thread to write and another to read, but is not user-level accessible. In contrast, applicant's claim 1 includes, "communicating between the first shred and the second shred via one or more shared registers that are adapted to be updateable by the first and the second shred." Note that Kissell's registers only allow a copy from one register to another or a write and a read, not a register that is updateable by both shreds, as in applicant's claim 1. Note that independent claims 35, 51, and 67 include comparable elements to claim 1.

Similarly, applicant's claim 17 includes, " a shared register to be directly accessible by a second user instruction from the first and the second shred to provide communication between the first shred and the second shred." In contrast, to Kissell's ThreadConfig register, which is only accessible by a FORK instruction from the first thread to copy the contents to a different register of a new thread, applicant's claim 17 allows for the shared register to be accessible by a user instruction from the first **and** the second thread. As noted in the Office Action, the inter-thread communication register is not accessible by a user-level instruction, but rather only privileged access. Note that independent claim 55 includes a similar element of being addressable by one or more application program instructions from a plurality of user-level threads.

Therefore, applicant respectfully submits that independent claims 1, 17, 35, 51, 55, and 67, as well as their dependent claims, are now in condition for allowance for at least the reasons stated above. If there are any additional charges, please charge Deposit Account No. 50-0221.

Respectfully submitted,  
Intel Corporation

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